

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/841,031	STARK, DAVID	
Examiner		Art Unit	2123	Page 1 of 1
Dwin M Craig				

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
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	D	US-			
	E	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Steve Haynal, Behrooz Parhami, "Arithmetic Structures for Inner-Product and Other Computations Based on a Latency-Free Bit-Serial Multiplier Design", IEEE 1997, pages 197-201.
	V	David A Dunn, Wei-Chung Hsu, "Instruction Scheduling for the HP PA-8000", IEEE 1996, pages 298-307.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.